

WEST**Searches for User *eamanze* (Count = 1457)**

Queries 1408 through 1457.

S #	Updt	Database	Query	Time	Comment
<u>S1457</u>	<u>U</u>	USPT	(((((memory or storage)and (write near operation))and (address same (flash near memor\$)))and (capacity or limit))and (frequency same error))and (memory near (status or indicator)))) and processor	2002-06-05 15:49:13	
<u>S1456</u>	<u>U</u>	USPT	(((((memory or storage)and (write near operation))and (address same (flash near memor\$)))and (capacity or limit))and (frequency same error))and (memory near (status or indicator)))	2002-06-05 15:47:45	
<u>S1455</u>	<u>U</u>	USPT	((((memory or storage)and (write near operation))and (address same (flash near memor\$)))and (capacity or limit)) and (frequency same error)	2002-06-05 15:46:52	
<u>S1454</u>	<u>U</u>	USPT	(((((memory or storage)and (write near operation))and (address same (flash near memor\$)))and (capacity or limit)) and (error near frequency)	2002-06-05 15:46:06	
<u>S1453</u>	<u>U</u>	USPT	((((memory or storage)and (write near operation))and (address same (flash near memor\$))) and (capacity or limit)	2002-06-05 15:44:32	
<u>S1452</u>	<u>U</u>	USPT	((((memory or storage)and (write near operation))and (address same (flash near memor\$))) (capacity or limit)	2002-06-05 15:43:37	
<u>S1451</u>	<u>U</u>	USPT	((memory or storage)and (write near operation)) and (address same (flash near memor\$))	2002-06-05 15:42:49	
<u>S1450</u>	<u>U</u>	USPT	(memory or storage) and (write near operation)	2002-06-05 15:41:28	
<u>S1449</u>	<u>U</u>	USPT	(memory or storage) and (memory or storage)	2002-06-05 15:40:32	
<u>S1448</u>	<u>U</u>	USPT	memory or storage	2002-06-05	

15:39:25

<u>S1447</u>	<u>U</u>	USPT	((((((((360/26 360/47 360/53)!.CCLS. (386/2 386/47 386/13 386/85)!.CCLS. (324/210)!.CCLS. (365/200 365/201) .CCLS. (714/718 714/719 714/720 714/721 714/722 714/723)!.CCLS.))and (memory or storage))and (write near operation))and (address same (flash near memor\$)))and (capacity or limit))and error)and status)and processor)and (memory same status)) and (pre-determined or predetermined or pre-set or preset)	2002-06-05 15:33:09
<u>S1446</u>	<u>U</u>	USPT	((((((((360/26 360/47 360/53)!.CCLS. (386/2 386/47 386/13 386/85)!.CCLS. (324/210)!.CCLS. (365/200 365/201) .CCLS. (714/718 714/719 714/720 714/721 714/722 714/723)!.CCLS.))and (memory or storage))and (write near operation))and (address same (flash near memor\$)))and (capacity or limit))and error)and status)and processor)and (memory same status)) and pre?determined	2002-06-05 15:32:27
<u>S1445</u>	<u>U</u>	USPT	((((((((360/26 360/47 360/53)!.CCLS. (386/2 386/47 386/13 386/85)!.CCLS. (324/210)!.CCLS. (365/200 365/201) .CCLS. (714/718 714/719 714/720 714/721 714/722 714/723)!.CCLS.))and (memory or storage))and (write near operation))and (address same (flash near memor\$)))and (capacity or limit))and error)and status)and processor) and (memory same status)	2002-06-05 15:30:57
<u>S1444</u>	<u>U</u>	USPT	((((((((360/26 360/47 360/53)!.CCLS. (386/2 386/47 386/13 386/85)!.CCLS. (324/210)!.CCLS. (365/200 365/201) .CCLS. (714/718 714/719 714/720 714/721 714/722 714/723)!.CCLS.))and (memory or storage))and (write near operation))and (address same (flash near memor\$)))and (capacity or limit))and error)and status) and processor	2002-06-05 15:30:21
<u>S1443</u>	<u>U</u>	USPT	((((((((360/26 360/47 360/53)!.CCLS. (386/2 386/47 386/13 386/85)!.CCLS. (324/210)!.CCLS. (365/200 365/201) .CCLS. (714/718 714/719 714/720 714/721 714/722 714/723)!.CCLS.))and (memory or storage))and (write near operation))and (address same (flash near memor\$)))and (capacity or limit))and error	2002-06-05 15:29:15

)and status) and indicator	
<u>S1442</u>	<u>U</u>	USPT	(((((((360/26 360/47 360/53)!.CCLS. (386/2 386/47 386/13 386/85)!.CCLS. (324/210)!.CCLS. (365/200 365/201)!..CCLS. (714/718 714/719 714/720 714/721 714/722 714/723)!.CCLS.))and (memory or storage))and (write near operation))and (address same (flash near memor\$)))and (capacity or limit))and error) and status	2002-06-05 15:28:39
<u>S1441</u>	<u>U</u>	USPT	(((((((360/26 360/47 360/53)!.CCLS. (386/2 386/47 386/13 386/85)!.CCLS. (324/210)!.CCLS. (365/200 365/201)!..CCLS. (714/718 714/719 714/720 714/721 714/722 714/723)!.CCLS.))and (memory or storage))and (write near operation))and (address same (flash near memor\$)))and (capacity or limit))and error) and (memory near (status or indicator))	2002-06-05 15:19:46
<u>S1440</u>	<u>U</u>	USPT	(((((((360/26 360/47 360/53)!.CCLS. (386/2 386/47 386/13 386/85)!.CCLS. (324/210)!.CCLS. (365/200 365/201)!..CCLS. (714/718 714/719 714/720 714/721 714/722 714/723)!.CCLS.))and (memory or storage))and (write near operation))and (address same (flash near memor\$)))and (capacity or limit)) and error	2002-06-05 15:18:04
<u>S1439</u>	<u>U</u>	USPT	(((((((360/26 360/47 360/53)!.CCLS. (386/2 386/47 386/13 386/85)!.CCLS. (324/210)!.CCLS. (365/200 365/201)!..CCLS. (714/718 714/719 714/720 714/721 714/722 714/723)!.CCLS.))and (memory or storage))and (write near operation))and (address same (flash near memor\$)))and (capacity or limit)) and (error near frequency)	2002-06-05 15:17:40
<u>S1438</u>	<u>U</u>	USPT	(((((((360/26 360/47 360/53)!.CCLS. (386/2 386/47 386/13 386/85)!.CCLS. (324/210)!.CCLS. (365/200 365/201)!..CCLS. (714/718 714/719 714/720 714/721 714/722 714/723)!.CCLS.))and (memory or storage))and (write near operation))and (address same (flash near memor\$))) and (capacity or limit)	2002-06-05 15:16:40
<u>S1437</u>	<u>U</u>	USPT	(((360/26 360/47 360/53)!.CCLS. (386/2 386/47 386/13 386/85)!.CCLS. (324/210)!.CCLS. (365/200 365/201)!..CCLS. (714/718 714/719 714/720	2002-06-05 15:15:45

			714/721 714/722 714/723)!.CCLS.))and (memory or storage))and (write near operation)) and (address same (flash near memor\$))	
<u>S1436</u>	<u>U</u>	USPT	(((360/26 360/47 360/53)!.CCLS. (386/2 2002-06-05 386/47 386/13 386/85)!.CCLS. (324/210 15:14:28).!.CCLS. (365/200 365/201)!.CCLS. (714/718 714/719 714/720 714/721 714/722 714/723)!.CCLS.))and (memory or storage)) and (write near operation)	
<u>S1435</u>	<u>U</u>	USPT	(((360/26 360/47 360/53)!.CCLS. (386/2 2002-06-05 386/47 386/13 386/85)!.CCLS. (324/210 15:13:39).!.CCLS. (365/200 365/201)!.CCLS. (714/718 714/719 714/720 714/721 714/722 714/723)!.CCLS.)) and (memory or storage)	
<u>S1434</u>	<u>U</u>	USPT	((360/26 360/47 360/53)!.CCLS. (386/2 2002-06-05 386/47 386/13 386/85)!.CCLS. (324/210 14:49:48).!.CCLS. (365/200 365/201)!.CCLS. (714/718 714/719 714/720 714/721 714/722 714/723)!.CCLS.)	
<u>S1433</u>	<u>U</u>	USPT	((((((((((((data and transmit\$)and 2002-06-02 (integrated near circuit))and (video near 20:04:38 graphics))and (data near stream))and block)and (sequence same data same set))and encoder)and (transmission same signal))and (transmission near signal))and capture)and (data smame error same indicat\$))and register)and pixel)and location)and differential) and receiver	
<u>S1432</u>	<u>U</u>	USPT	((((((((((((data and transmit\$)and 2002-06-02 (integrated near circuit))and (video near 20:04:12 graphics))and (data near stream))and block)and (sequence same data same set))and encoder)and (transmission same signal))and (transmission near signal))and capture)and (data smame error same indicat\$))and register)and pixel)and location) and differential	
<u>S1431</u>	<u>U</u>	USPT	((((((((((((data and transmit\$)and 2002-06-02 (integrated near circuit))and (video near 20:03:25 graphics))and (data near stream))and block)and (sequence same data same set))and encoder)and (transmission same signal))and (transmission near signal))and capture)and (data smame error same indicat\$))and register)and pixel)and location) and position	